

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MURATA et al

Serial No.:

Filed: January 14, 2004

For: A Semiconductor Integrated Circuit Device And
A Method of Manufacturing The Same

Art Unit:

Examiner:

PRELIMINARY AMENDMENT

Mail Stop: Amendment (No Fee)
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 14, 2004

Sir:

The following amendments and remarks are respectfully requested in connection with the above-identified application as listed below and as set forth on the following pages:

A substitute specification along with a marked-up copy of the original specification is attached;

Amendments to the Claims;

Amendments to the Abstract;

Remarks are included following the amendments.